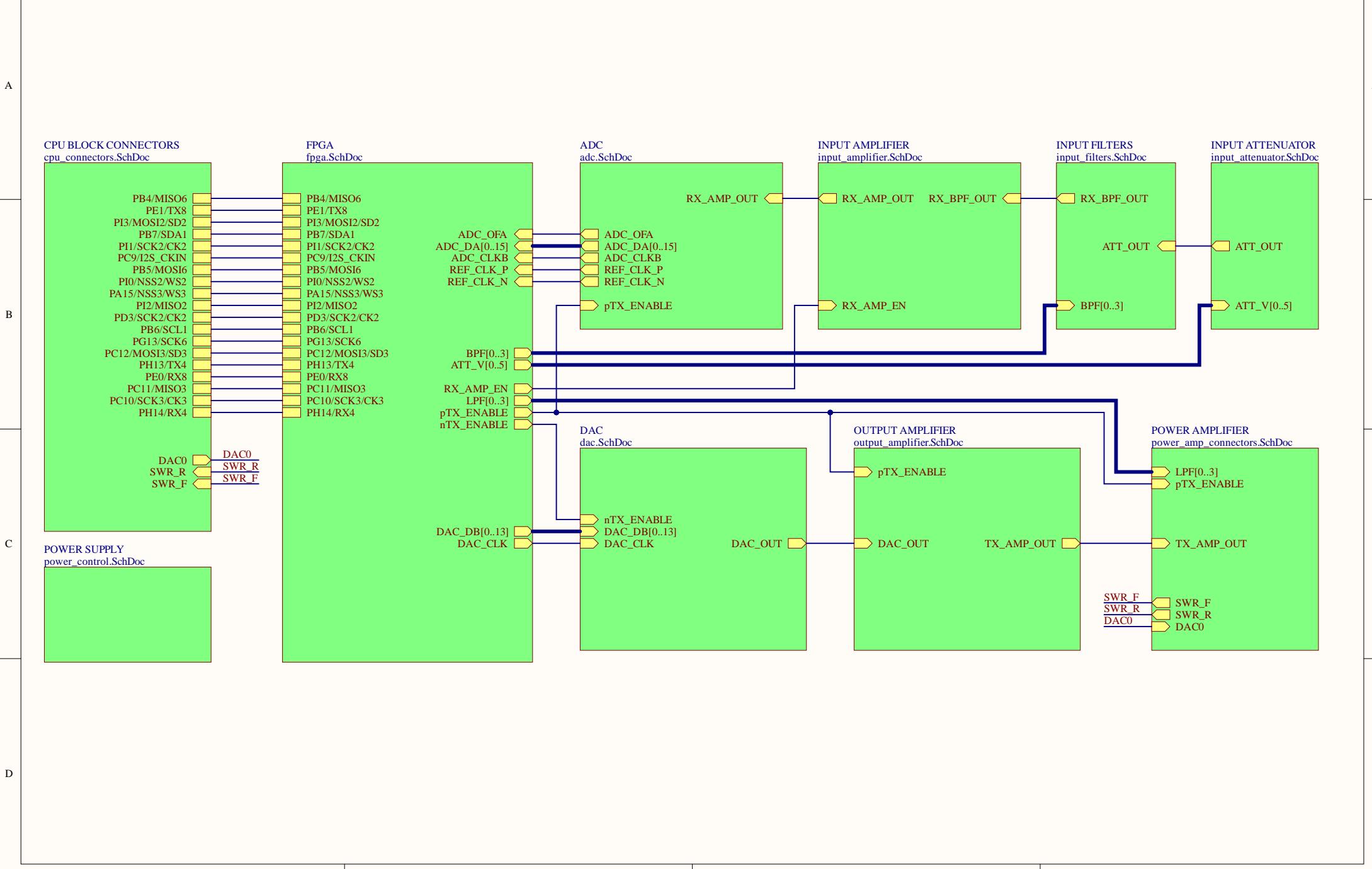


1 2 3 4



1

2

3

4

A

A

B

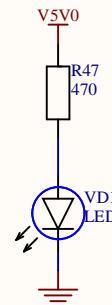
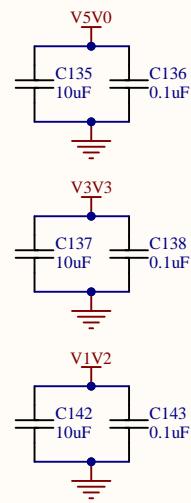
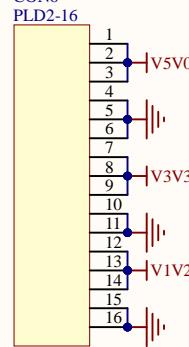
B

C

C

D

D



V3V3

F12

BLM21

C139

0.1uF

GND

1

DA7

MIC5504-2.5YM5-TR

VIN

VOUT

EN

GND

NC

2

5

V2V5

4

C140

10uF

C141

0.1uF

GND

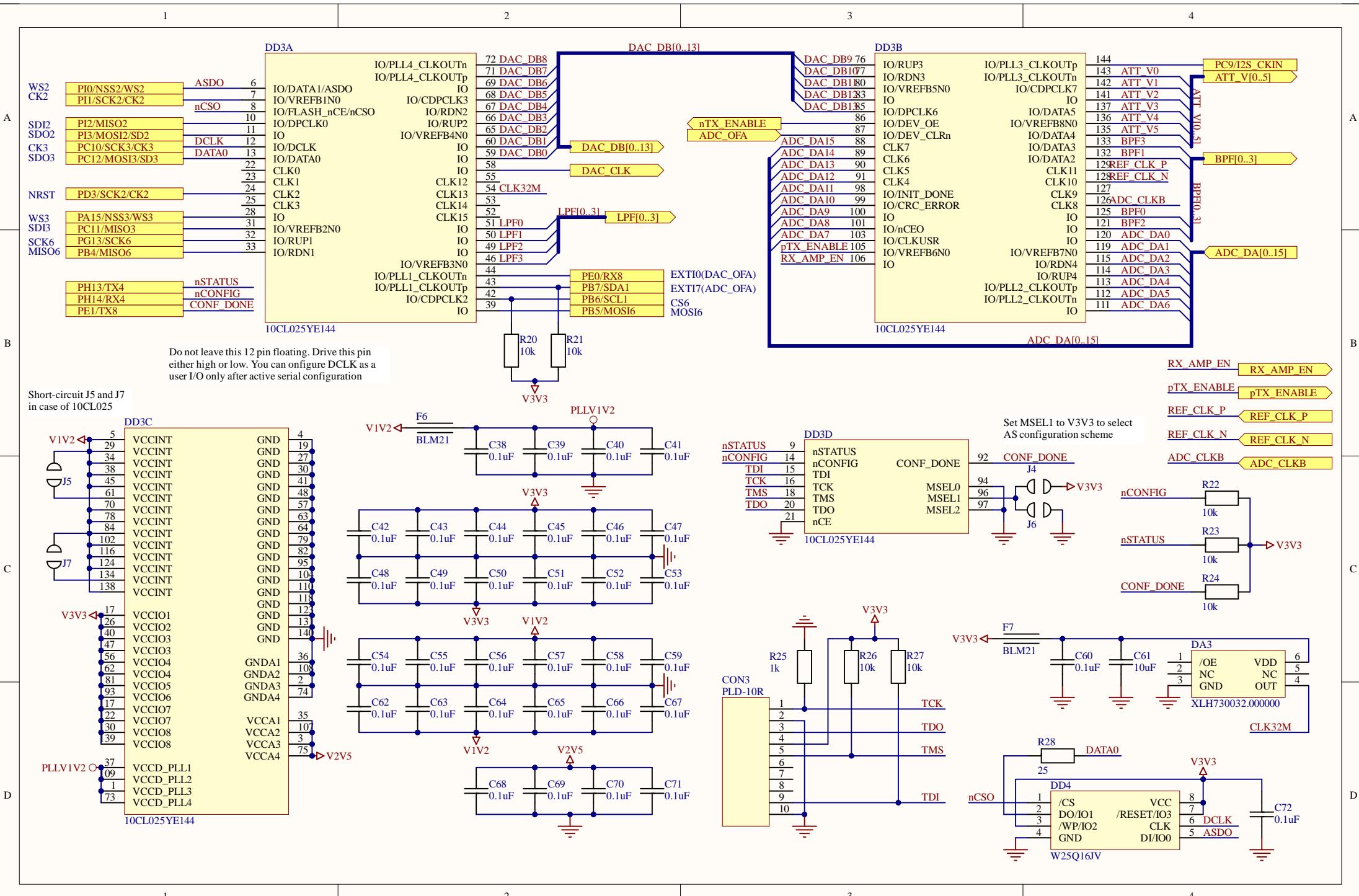
3

1

2

3

4



1

2

3

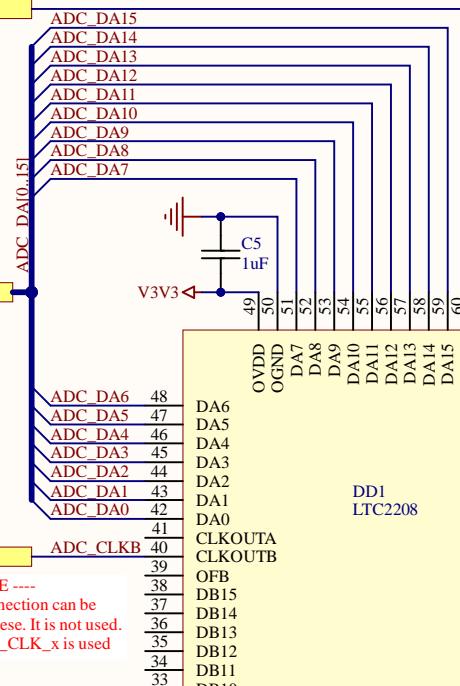
4

REF_CLK_N
REF_CLK_P

ADC_OFA

ADC_DA[0..15]

ADC_CLKB
--- TO IMPROVE ---
ADC_CLKB connection can be deleted in next release. It is not used.
Instead diff REF_CLK_x is used

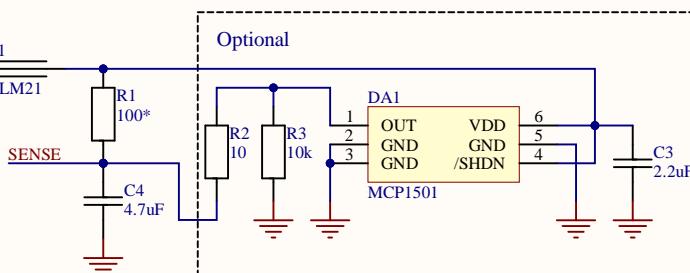
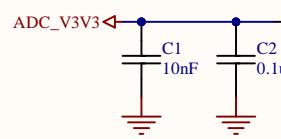


--- TO IMPROVE ---
Add jumpers in order to allow Randomizer selection in next releases

SENSE
GND
VCM
GND
VDD
GND
AIN+
AIN-
GND
GND
ENC+
ENC-
GND
VDD
VDD

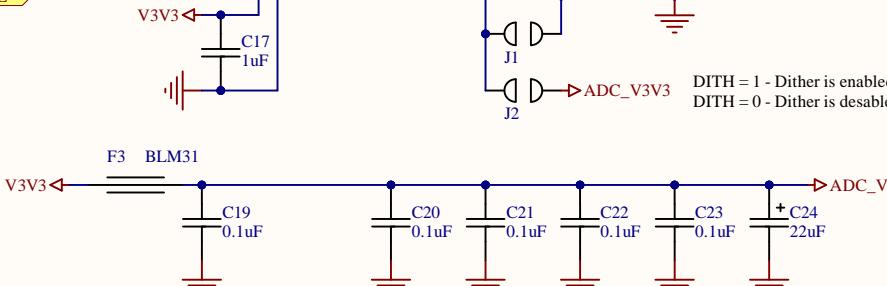
DD1 LTC2208

R4 installed only to select 2's complement output format and disable the clock duty cycle stabilizer.
Don't install R5



SHDN = low - Normal Operation
SHDN = high - Powered Down

pTX_ENABLE



A

A

B

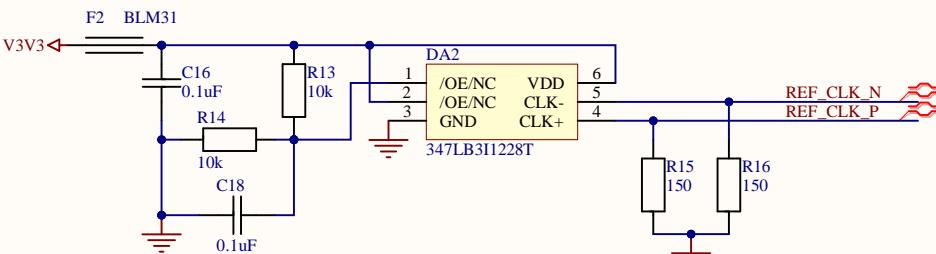
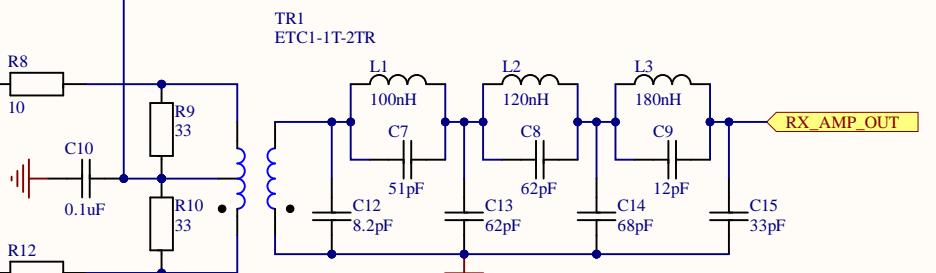
B

C

C

D

D

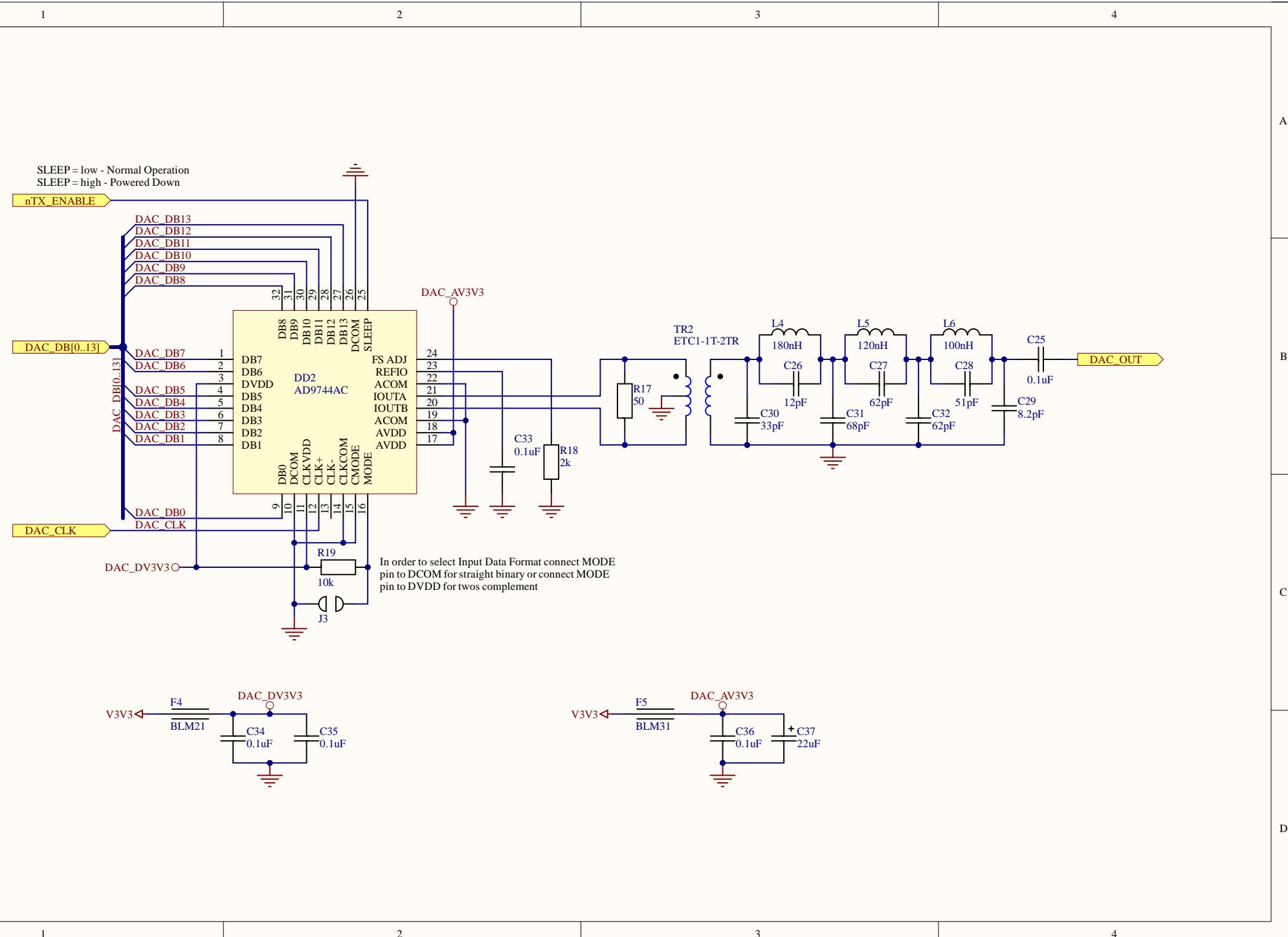


1

2

3

4



A

A

B

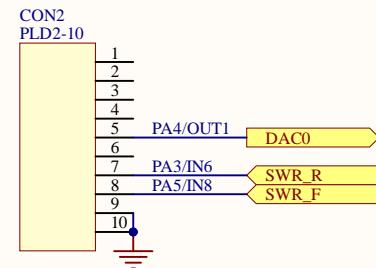
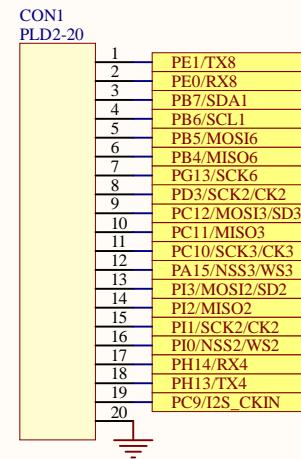
B

C

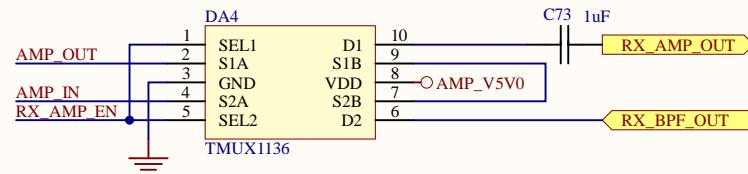
C

D

D

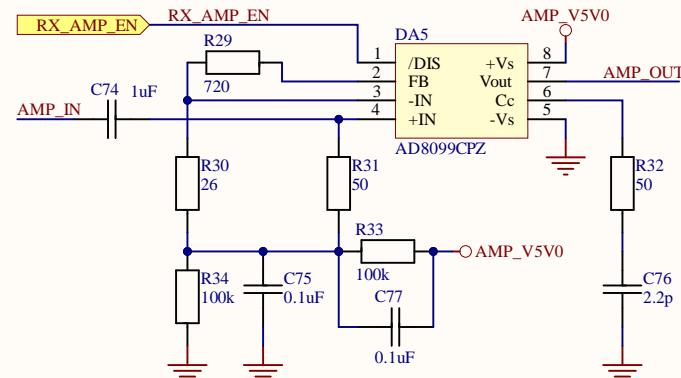


A

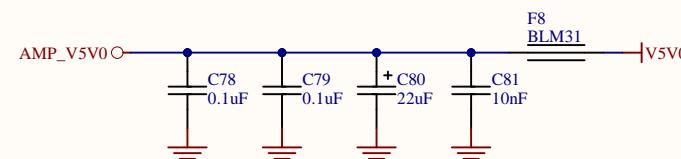


---- TO IMPROVE ----
 /DIS pin should be unconnected to reduce noise (see datasheet). Use transistor to power on/off DA5

B

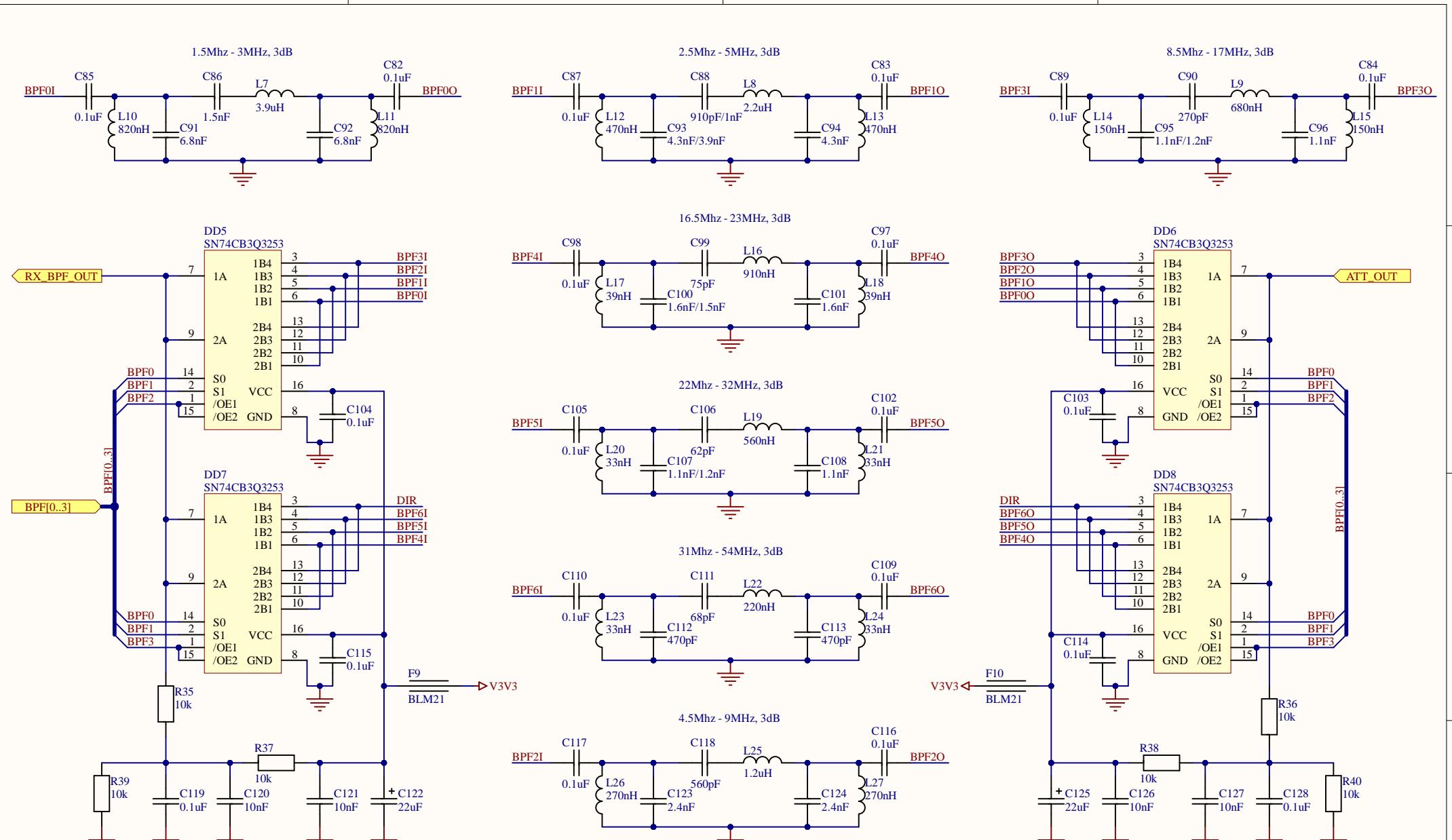


C



D

1 2 3 4



1 2 3 4

1 2 3 4

A

A

B

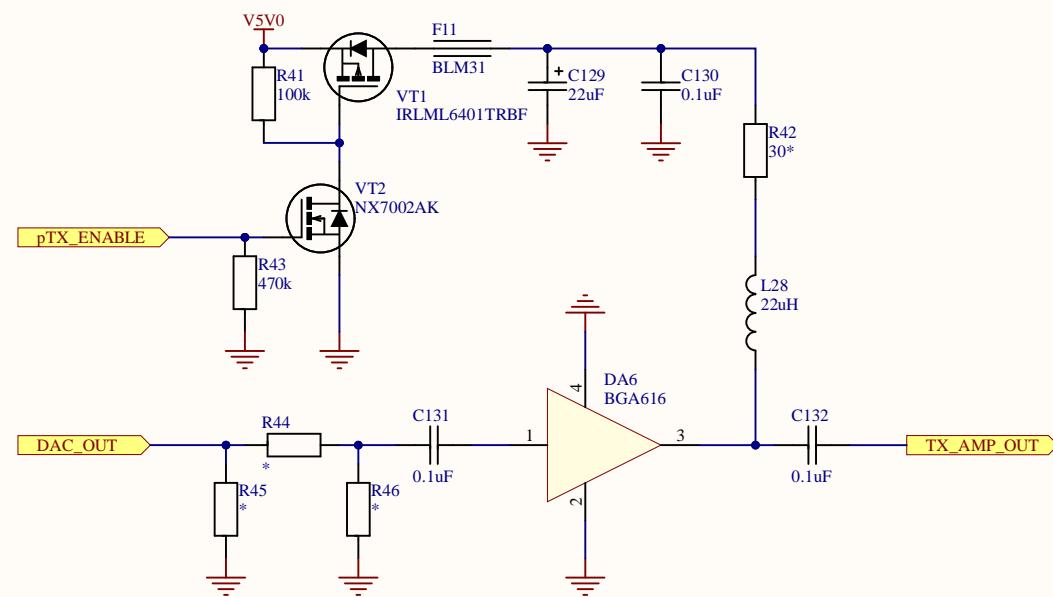
B

C

C

D

D



1 2 3 4

A

A

B

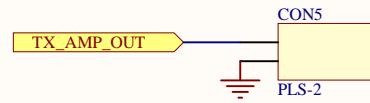
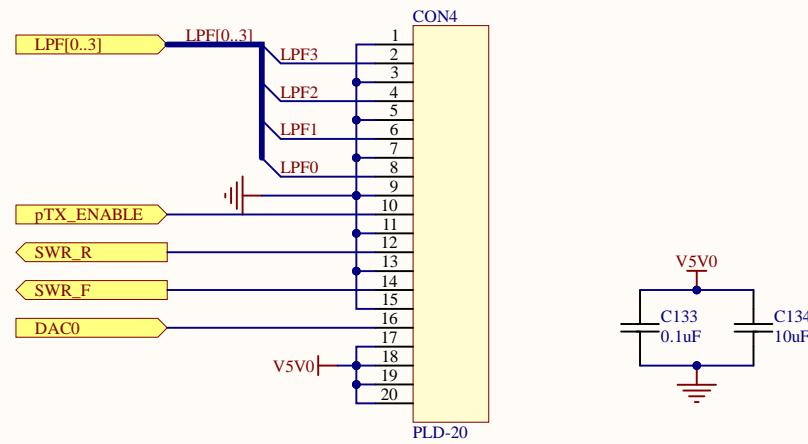
B

C

C

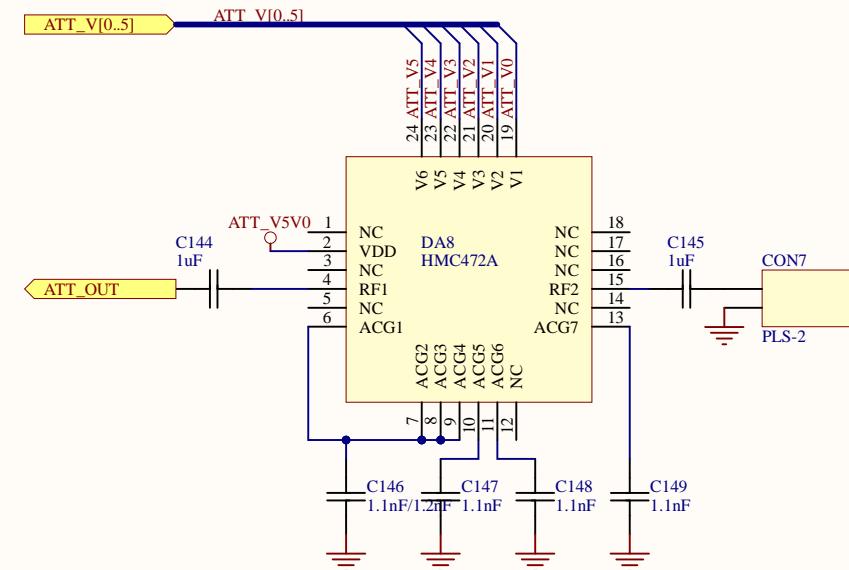
D

D



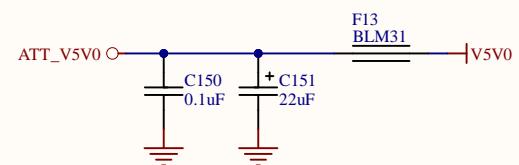
A

A



B

B



C

C

D

D

Board Stack Report